



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Thomas A. Dye; Manuel J. Alvarez II; Peter Geiger
Assignee: Interactive Silicon, Inc.
Title: Memory Module Including Scalable Embedded Parallel Data
Compression And Decompression Engines
Serial No.: 09/616,480 Filing Date: 7/14/2000
Examiner: Unknown Group Art Unit: Unknown
Docket No.: 40532-P022US
(f/k/a 5143-01801)

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Dallas, Texas
June 9, 2003

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**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97(B)**

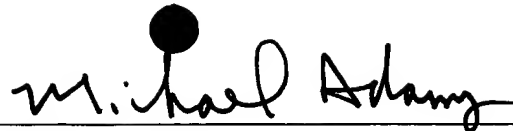
Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

These documents include documents that were cited in related patent(s) and/or patent application(s).



Michael P. Adams
Attorney for Applicant(s)
Reg. No. 34,763

CERTIFICATION UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence (along with any item referred to as being enclosed herewith) is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Mail Stop DD, Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 9, 2003.



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